Amendments to the Specification

Please amend lines 5-8 of page 36 of the specification to read:

FIG. 10 illustrates a block diagram 320(a) of the deinterleaving address generator 320 generating a read address for reading code symbols written in the input buffer 310, for N_{EP}=408 (m=7), 792 (m=8), 1560 (m=9), 3096 (m=10), 6168 (m=11) and 12312 (m=12), and J=4.

Please amend lines 17-19 of page 37 of the specification to read:

FIG. 11 illustrates a block diagram 320b of the deinterleaving address generator 320 generating a read address for reading code symbols written in the input buffer 310, for N_{EP}=3238 (m=10 and J=3).

Please amend lines 16-18 of page 38 of the specification to read:

FIG. 12 illustrates a block diagram 320c of the deinterleaving address generator 320 generating a read address for reading code symbols written in the input buffer 310, for N_{EP}=3864 (m=11 and J=2).

Please amend lines 14-16 of page 39 of the specification to read:

FIG. 13 illustrates a block diagram 320d of the deinterleaving address generator 320 generating a read address for reading code symbols written in the input buffer 310, for N_{EP}=4632 (m=11 and J=3).

Please amend lines 22-25 of page 39 of the specification to read:

A BRO operator ~~422~~ 472 groups bits obtained by dividing the code symbol index k by 2^m, performs a BRO operation on a row index for symbols of each group by the m bits, and calculates a row index r_k for the code symbol index k.

Please amend lines 14-16 of page 40 of the specification to read:

FIG. 14 illustrates a block diagram 320e of the deinterleaving address generator 320 generating a read address for reading code symbols written in the input buffer 310, for N_{EP}=9240 (m=12 and J=3).

Please amend lines 12-14 of page 41 of the specification to read:

FIG. 15 illustrates a block diagram 320f of the deinterleaving address generator 320 generating a read address for reading code symbols written in the input buffer 310, for N_{EP}=15384 (m=13 and J=2).